

## Module Specification

| 1. Factual information  |                                  |                                |           |
|---|----------------------------------|--------------------------------|-----------|
| <b>Module title</b>   | <b>MT372: Parallel Computing</b> | <b>Level</b>                   | <b>3</b>  |
| <b>Module tutor</b>   | TBA                              | <b>Credit value</b>            | <b>10</b> |
| <b>Module type</b>  | Taught                           | <b>Notional learning hours</b> | <b>3</b>  |
| 2. Rationale for the module and its links with other modules  |                                  |                                |           |
| <p>The module is a comprehensive study of parallel computing techniques, parallel programming and performance tuning. Topics covered include: fundamentals of parallel, concurrent and distributed computing systems, performance and limitations of these systems, and parallelism paradigms. In addition to these topics the software needs and support for parallel processor systems are covered in details. This includes programming languages, simulation and tracing tools. Students will examine a range of topics involved in using parallel operations to improve computational performance, parallel architectures, parallel algorithms and parallel programming languages; Architectures covered include vector computers, multiprocessors, network computers, and data flow machines.</p> |                                  |                                |           |
| 3. Aims of the module   |                                  |                                |           |
| <p>The module aims to give solid understanding about the following:</p> <ol style="list-style-type: none"> <li>1. The fundamentals of parallel computing.</li> <li>2. Parallel operation.</li> <li>3. The different core concepts behind the hardware layer of a computer system.</li> <li>4. Performance and limitations of parallel systems</li> <li>5. The processor's architecture of parallel systems and its interconnection networks.</li> <li>6. The parallel algorithms.</li> </ol>  |                                  |                                |           |
| 4. Pre-requisite modules or specified entry requirements  |                                  |                                |           |
| <p>Students are expected to have completed study of M269 &amp; M251</p>   |                                  |                                |           |

| <b>5. Intended learning outcomes</b>   |   |
|--|---|
| <b>A. Knowledge and understanding</b>  | <b>Learning and teaching strategy</b>   |
| <p>Upon completing this module, students will be able to:</p> <ul style="list-style-type: none"> <li>A1. Understand of the fundamental concept and issues of parallel computing</li> <li>A2. Recognize parallel programming experience solving computationally intensive problems in a variety of disciplines</li> <li>A3. Understand the related implementations and measurements of performance and constraints of parallel computing</li> </ul> | <ul style="list-style-type: none"> <li>• 25% face-to-face tutorial sessions</li> <li>• TMA</li> <li>• Module learning text book and support material</li> </ul> |
| <b>B. Cognitive skills</b>   | <b>Learning and teaching strategy</b>   |
| <p>Upon completing this module, students will be able to:</p> <ul style="list-style-type: none"> <li>B1. Practice Parallel programming platforms</li> <li>B2. Apply Principles of parallel algorithm design</li> <li>B3. Illustrate Basic communication operations</li> <li>B4. Perform Analytical modelling of parallel programs</li> <li>B5. Develop Programming using the message-passing paradigm (MPI)</li> </ul>                             | <ul style="list-style-type: none"> <li>• 25% face-to-face tutorial sessions</li> <li>• TMA</li> <li>• Module learning booklets and support material</li> </ul>  |
| <b>C. Practical and professional skills</b>  | <b>Learning and teaching strategy</b>   |
| <p>Upon completing this module, students will be able to:</p> <ul style="list-style-type: none"> <li>C1. Apply the techniques and theorems in real applications.</li> <li>C2. Analyze specific data and information to build the parallel models</li> <li>C3. Apply the tools studied concerning parallel computing to solve a real problem.</li> </ul>  | <ul style="list-style-type: none"> <li>• 25% face-to-face tutorial sessions</li> <li>• TMA</li> <li>• Module learning booklets and support material</li> </ul>  |
| <b>D Key transferable skills</b>   | <b>Learning and teaching strategy</b>   |
| <p>Upon completing this module, students will be able to:</p>  |   |

| D Key transferable skills   | Learning and teaching strategy   |
|---|--|
| <p>D1. Gather data from various sources, including the electronic media, such as internet.</p> <p>D2. Choose a case study from the real world and apply the techniques studied.</p> <p>D3. Show responsibility for the preparation of the case study and manage the presentation schedule of his/her work.</p> <p>D4. Exercise research skills, such as data collection, tabulation, analysis, report presentation and class discussions.</p> | <ul style="list-style-type: none"> <li>• 25% face-to-face tutorial sessions</li> <li>• TMA</li> <li>• Module learning booklets and support material</li> </ul> |

| 6. Indicative content.   |
|--|
| <ul style="list-style-type: none"> <li>- Introduction to parallel computing</li> <li>- Fundamentals of Inter-processor Communication</li> <li>- PRAM model and basic algorithms</li> <li>- Overview of parallel algorithms</li> <li>- Parallel algorithms Complexity and models</li> <li>- Shared-memory algorithms and implementations.</li> <li>- Classification of Parallel Machines SISD , MISD , SIMD , MIMD , SPMD</li> <li>- Interconnection Networks Basics</li> <li>- Shared Memory and Message Passing EREW , CREW , ERCW , CRCW</li> <li>- Interconnection Networks (• All-to-All • Mesh • Rings • Hypercube)</li> <li>- Shuffle Exchange - Cube Connected Cycles - Metrics for Interconnection Networks</li> <li>- Parallel Algorithm Construction</li> <li>- Algorithmic Parallelism</li> <li>- Geometric Parallelism</li> <li>- Asynchronous / Relaxed Parallelism</li> <li>- Defining Speedup and Efficiency</li> <li>- Factors That Limit Speedup</li> </ul> |

| 7. Assessment strategy, assessment methods and their relative weightings |
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| TMA: 20%<br>MTA: 30%<br>Exam: 50%  |

| 8. Mapping of assessment tasks to learning outcomes |                   |    |    |    |    |    |    |    |    |    |    |    |    |    |    |
|---|-------------------|----|----|----|----|----|----|----|----|----|----|----|----|----|----|
| Assessment tasks                                    | Learning Outcomes |    |    |    |    |    |    |    |    |    |    |    |    |    |    |
|   | A1                | A2 | A3 | B1 | B2 | B3 | B4 | B5 | C1 | C2 | C3 | D1 | D2 | D3 | D4 |
| TMA   | ✓                 |    |    |    | ✓  |    |    | ✓  |    |    | ✓  | ✓  | ✓  | ✓  | ✓  |
| MTA   | ✓                 | ✓  | ✓  | ✓  |    |    |    | ✓  |    | ✓  |    |    |    |    |    |
| Exam  | ✓                 | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  | ✓  |    | ✓  |    |    |    |

| 9. Teaching staff associated with the module |               |
|--|---------------|
| Tutor's name and contact details             | Contact hours |
| TBA  |               |

| 10. Key reading list                                       |                                 |                                    |                |                     |
|--|---------------------------------|------------------------------------|----------------|---------------------|
| Author   | Year                            | Title                              | Publisher      | Location            |
| Ananth Grama, Anshul Gupta, George Karypis and Vipin Kumar | 2003<br>2 <sup>nd</sup> Edition | Introduction to Parallel Computing | Addison Wesley | ISBN: 0-201064865-2 |

| 11. Other indicative text (e.g. websites)                         |
|---|
| <a href="https://lms.arabou.edu.kw">https://lms.arabou.edu.kw</a> |